



# A. J. INSTITUTE OF ENGINEERING AND TECHNOLOGY

(A Unit of Laxmi Memorial Education Trust®)

Approved by AICTE New Delhi, Affiliated to VTU Belagavi, Recognised by Govt. of Karnataka

NH-66, Kottara Chowki, Mangaluru -575006, D. K. District, Karnataka, INDIA

ACCREDITED BY NAAC & NBA (BE: CV, CSE, ECE, ISE, ME)

Date:11/04/2026

## REPORT ON

### Two Day Workshop “HANDS-ON HARDWARE: MASTERING LINUX BASICS & DIGITAL LOGIC”

<b>Name of the Program:</b>	HANDS-ON HARDWARE: MASTERING LINUX BASICS & DIGITAL LOGIC	<b>Program Dates &amp; Timings:</b>	10/04/26 to 11/04/2026  9.00AM – 05.00PM
<b>Name &amp; Details of the Resource Person:</b>	<ol style="list-style-type: none"> <li>Mr.Manamohana K, Assistant Professor- Selection Grade, MIT, Manipal</li> <li>Mr.Akshay J Rai, Verification Engineer, Miraфра Technologies</li> </ol>		
<b>Organized by</b>	Dept. of ECE	<b>In Association with (clubs/Dept.)</b>	EVSA- Student Association
<b>Number of Participants</b>	Students	Faculty	External participants
<b>Program Outcome (PO) Mapping</b>	PO1. PO2, PO3, PO5		
<b>Coordinators</b>	Dr.Suhandas , Mr.Manjukiran B		
<b>Expenditure</b>	Rs.12,000/-	<b>Social Media Link</b>	

#### About the Program:

The Department of Electronics and Communication Engineering at A. J. Institute of Engineering and Technology, in collaboration with EVSA – Student Association, organized a two-day hands-on workshop titled “Hands-on Hardware: Mastering Linux Basics & Digital Logic” on 10th and 11th April 2026.

This workshop aims to provide participants with practical exposure to Linux operating systems and fundamental digital logic design concepts. Through interactive sessions and hands-on activities, participants gained essential skills in command-line operations, circuit design and hardware–software integration.

The sessions was led by experienced resource persons from academia and industry, offering valuable insights into real-world applications, embedded systems, and emerging technologies. The workshop is



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designed to enhance technical competency, problem-solving abilities, and industry readiness among students.

**Objectives:** The program enable students/ faculty to:

1. To bridge the gap between software (Linux environment) and hardware (digital electronics) concepts for real-world applications.
2. To enhance problem-solving and debugging skills in both Linux-based systems and digital circuit design.
3. To prepare participants for advanced topics in embedded systems, VLSI, IoT, and hardware-software integration.

**Outcomes:** On successful completion of program of this workshop the student should able to:

1. Integrate Linux-based software skills with digital hardware concepts to implement simple real-world applications.
2. Demonstrate effective problem-solving and debugging techniques in both Linux environments and digital circuit setups.
3. Interpret and apply foundational concepts required for advanced study in embedded systems, VLSI, IoT, and hardware–software co-design.

**Articulation Matrix:**

Course Outcomes	Program Outcomes											
	1	2	3	4	5	6	7	8	9	10	11	12
1	3	3	3		3							
2	3	3	3		3							
3	3	3	3		3							
<b>Average</b>	3	3	3		3							

## Sustainable Development Goal (SDG) Mapping:

This program aligns with the following United Nations Sustainable Development Goals (SDGs):

- **SDG 9: Industry, Innovation, and Infrastructure** – Providing insights into industrial processes and technological advancements.

### Description on SDG Impact:

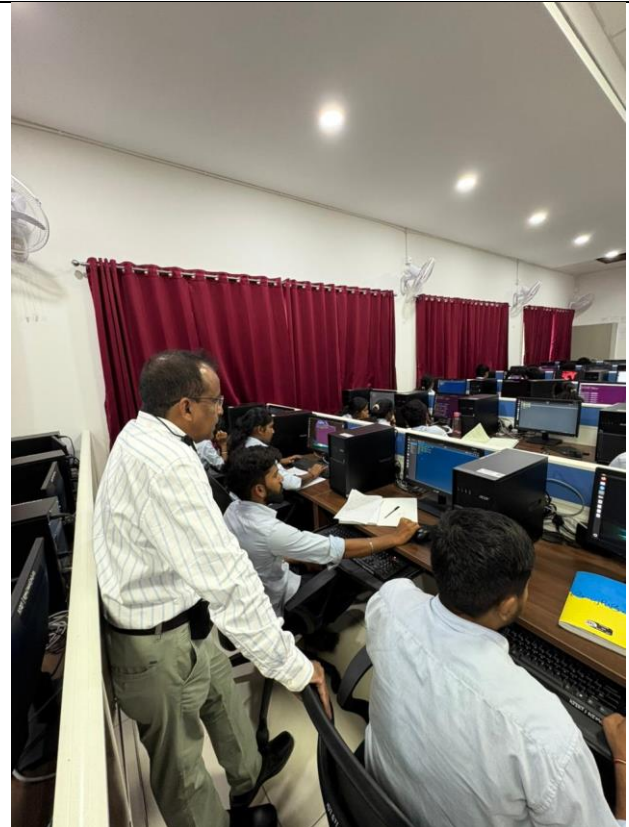
This workshop strongly supports SDG 9: Industry, Innovation and Infrastructure by equipping participants with essential skills in Linux-based systems and digital logic design, which are foundational to modern industrial and technological ecosystems.

Through hands-on exposure to hardware and software integration, the workshop fosters innovation by enabling participants to design, analyze and implement practical solutions relevant to real-world industrial applications. The focus on problem-solving, system-level thinking and debugging prepares learners to contribute effectively to the development of robust and scalable technological infrastructure.

### Photos:



Inauguration Function



Session by Mr. Manamohana K



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Session by Mr.Akshay Rai



## Event Coordinators

1.Dr.Suhandas

2.Mr.Manjukiran B

HOD/

Dean Academics

Principal

Committee Coordinator

**For Information:** Vice President, LMET

**Copy To:** IQAC – AJIET